# Radial Leaded PTC OZRR Series



RoHS 2 Compliant

#### **Application**

Wide variety of electronic equipment

#### **Product Features**

- Low hold current, Solid state, Radial leaded product ideal for up to 60VDC
- AEC-Q Compliant
- Meets Bel automotive qualification\*
  - \* Largely based on internal AEC-Q test plan

#### **Operating (Hold Current) Range**

50mA - 3.75A

**Maximum Voltage** 

**Temperature Range** 

60VDC

-40°C to 85°C

#### **Agency Approval**

TUV (Std. EN60738-1-1, Cert. R50102187)

UL Recognized Component (Std. UL1434, File E305051)

UL Conditions of Acceptability:

- 1. These devices have been investigated for use in safety circuits and are suitable as a limiting device.
- These devices have been calibrated to limit the current to 8 amps within 5 seconds, per ANSI/NFPA 70, "National Electrical Code".

LEAD FREE =



HALOGEN FREE = HF



AEC-Q Compliant

TÜV C TÜ'US

#### **Electrical Characteristics (23°C)**

	Part Number (Bulk)	(:urrent	Trip	Max Time to	Max	Rated	Typical	Resistance Tolerance		Agency Approvals	
			Current	Trip @ 5xIH	Current	Voltage	Power	Rmin	R1max	c <b>71</b> 2°us	ΤÜV
		Iн, А	Iτ, A	Seconds	Imax, A	Vmax, Vdc	Pd, W Ohms	Ohms	Ohms	C 744 US	TÜV
Α	0ZRR0005FF1E	0.05	0.10	5.0	40	60	0.26	7.30	20.00	Υ	
В	0ZRR0010FF1E	0.10	0.20	4.0	40	60	0.38	2.50	7.50	Υ	Υ
С	0ZRR0017FF1E	0.17	0.34	3.0	40	60	0.48	2.00	8.00	Υ	Υ
D	0ZRR0020FF1E	0.20	0.40	2.2	40	60	0.41	1.83	4.40	Υ	Υ
Е	0ZRR0025FF1E	0.25	0.50	2.5	40	60	0.45	1.25	3.00	Υ	Υ
F	0ZRR0030FF1E	0.30	0.60	3.0	40	60	0.49	0.88	2.10	Υ	Υ
G	0ZRR0040FF1E	0.40	0.80	3.8	40	60	0.56	0.55	1.29	Υ	Υ
Н	0ZRR0050FF1E	0.50	1.00	4.0	40	60	0.77	0.50	1.17	Υ	Υ
I	0ZRR0065FF1E	0.65	1.30	5.3	40	60	0.88	0.31	0.72	Υ	Υ
J	0ZRR0075FF1E	0.75	1.50	6.3	40	60	0.92	0.25	0.60	Υ	Υ
K	0ZRR0090FF1E	0.90	1.80	7.2	40	60	0.99	0.20	0.47	Υ	Υ
L	0ZRR0110FF1A	1.10	2.20	8.2	40	60	1.50	0.15	0.38	Υ	Υ
M	0ZRR0135FF1A	1.35	2.70	9.6	40	60	1.70	0.12	0.30	Υ	Υ
Ν	0ZRR0160FF1A	1.60	3.20	11.4	40	60	1.90	0.09	0.22	Υ	Υ
0	0ZRR0185FF1A	1.85	3.70	12.6	40	60	2.10	80.0	0.19	Υ	Υ
Р	0ZRR0250FF1A	2.50	5.00	15.6	40	60	2.50	0.05	0.13	Υ	Υ
Q	0ZRR0300FF1A	3.00	6.00	19.8	40	60	2.80	0.04	0.10	Υ	Υ
R	0ZRR0375FF1A	3.75	7.50	24.0	40	60	3.20	0.03	0.08	Υ	Υ

IH Hold Current-maximum current at which the device will not trip in still air at 23°C.

Trip current-minimum current at which the device will always trip in still air at 23°C.

Imax Maximum fault current device can withstand without damage at rated voltage (Vmax).

Vmax Maximum voltage device can withstand without damage at its rated current.

Pd Typical power dissipated by device when in tripped state in 23°C still air environment.

Rmin Minimum device resistance at 23°C

R1max Maximum device resistance at 23°C, 1 hour after initial device trip, or after being soldered to PCB in end application.



Specifications subject to change without notice

# Type 0ZRR Series

#### PTC's - Basic Theory of Operation / "Tripped" Resistance Explanation

Fundamentally, a Bel PTC consists of a block of polymeric material containing conductive filler and bonded between two conductive, planar terminations.

At currents below the device IHOLD rating, AND at temperatures below 100C, the PTC maintains a resistance value below its R1 MAX rating.

As the device's temperature approaches 130C, either due to an increase in ambient temperature or a current exceeding its I TRIP rating, volumetric expansion of the filled polymer breaks apart the majority of conductive pathways across the terminals created by chain contact of adjacent filler particles or device resistance increases sharply by several orders of magnitude.

At the much higher "Tripped" resistance, there is just enough leakage current to allow internal heating to "hold" the device in its tripped state (around 125C) until power is interrupted. Once power is removed, the PTC's core cools and contracts allowing conductive chains to reform and return the device to its low resistance state.

The catalog data for each device specifies a "Typical Power" value. This is the power required to exactly match the heat lost by the tripped device to its ambient surroundings at 23C. By Ohm's Law, power can be stated as:  $W = E^2R$ . Thus the approximate resistance of a "Tripped" PTC can be determined by:  $R = E^2W$ , where "E" is the voltage appearing across the PTC (usually the supply's open circuit voltage), and "W" is the Typical Power value for the particular PTC.

Since the PPTC acts to maintain a constant internal temperature, its apparent resistance will change based upon applied voltage and, to a lesser degree, ambient conditions. Consider the following example....

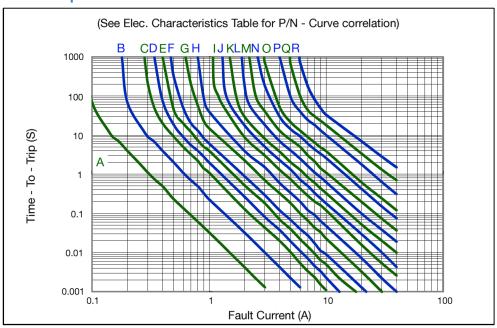
A PTC with a Typical Power of 1 watt protecting a circuit using a 60V supply will demonstrate an apparent, tripped resistance "R" of:

 $R = 60^2/1 = 3,600 \text{ ohms}$ 

This same tripped device when used to protect a 12V circuit would now present an apparent resistance of:  $R = 12^2/1 = 144$  ohms

The value for Typical Power is "typical" because any physical factors that affect heat loss (such as ambient temperature or air convection) will somewhat alter the level of power that the PTC needs to maintain its internal temperature. In short, PTCs do not exhibit a constant, quantifiable tripped resistance value.







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All dimensions in mm.

# Type 0ZRR Series

#### **Physical Specifications**

Lead material:

Matte tin plated copper, size / diameter as shown in Drawings and Table under Product Dimensions. Soldering characteristics

MIL-STD-202, Method 208H.

Insulating coating

Flame retardant epoxy, meets UL-94-V-0 requirements.

#### **PTC Marking**

"bel" or "b", , IH code and "RR".

#### **Product Dimensions**

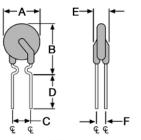


Fig.1 Lead Size : 24AWG Φ 0.51 mm Diameter

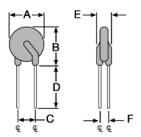
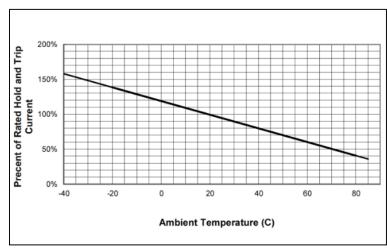


Fig.2 Lead Size : 20AWG Φ 0.81 mm Diameter

Part	Fig.	Α	В	С	D	Е	F
Number		Max	Max	Typical	Min	Max	Typical
0ZRR0005FF - 0ZRR0025FF	1	7.4	12.7	5.1	7.6	3.1	1.1
0ZRR0030FF	1	7.4	13.0	5.1	7.6	3.1	1.1
0ZRR0040FF	1	7.6	13.5	5.1	7.6	3.1	1.1
0ZRR0050FF	1	7.9	13.7	5.1	7.6	3.1	1.1
0ZRR0065FF	1	9.7	14.5	5.1	7.6	3.1	1.1
0ZRR0075FF	1	10.4	15.2	5.1	7.6	3.1	1.1
0ZRR0090FF	1	11.7	15.8	5.1	7.6	3.1	1.1
0ZRR0110FF	2	13.0	18.0	5.1	7.6	3.1	1.4
0ZRR0135FF	2	14.5	19.6	5.1	7.6	3.1	1.4
0ZRR0160FF	2	16.3	21.3	5.1	7.6	3.1	1.4
0ZRR0185FF	2	17.8	22.9	5.1	7.6	3.1	1.4
0ZRR0250FF	2	21.3	26.4	10.2	7.6	3.1	1.4
0ZRR0300FF	2	24.9	30.0	10.2	7.6	3.1	1.4
0ZRR0375FF	2	28.5	33.5	10.2	7.6	3.1	1.4

## **Thermal Derating Curve**



#### **Cautionary Notes**

- Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.
- These Polymer PTC (PPTC) devices are intended for protection against occasional overcurrent/ overtemperature fault conditions and may not be suitable for use in applications where repeated and/or prolonged fault conditions are anticipated.
- Avoid contact of PTC device with chemical solvent. Prolonged contact may adversely impact the PTC performance.
- These PTC devices may not be suitable for use in circuits with a large inductance, as the PTC trip can generate circuit voltage spikes above the PTC rated voltage.
- These devices are intended for use in DC voltage applications only. Use in AC voltage applications should be first discussed with Bel Fuse engineering.
- 6. Not recommended for use on potted or conformal coated PCB's. Restriction of free air flow could affect electrical performance and/or result in device failure. Consult Bel Fuse engineering.



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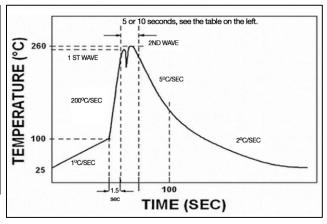
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### **Environmental Specifications**

Temperature cycling	JESD22 Method JA-104			
Biased humidity	MIL-STD-202 Method 103			
Operational life	MIL-STD-202 Method 108			
Terminal strength	AEC-Q200-004			
Resistance to solvents	MIL-STD-202 Method 215			
Mechanical shock	MIL-STD-202 Method 213			
Vibration	MIL-STD-202 Method 204			
Resistance to soldering heat	MIL-STD-202 Method 210			
Thermal shock	MIL-STD-202 Method 107			
Solderability	ANSI/J-STD-002			

#### **Soldering Parameters**

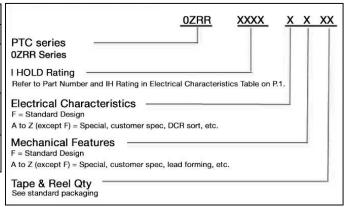
Lead-free Wave Soldering Profile						
Wave Solder Parameter						
Average ramp-up rate	200°C / second					
Heating rate during preheat	typical 1 - 2°C / second Max 4°C / second					
Final preheat temperature	within 125℃ of soldering temperature					
Peak temperature Tp	260℃					
Time within +0 $^{\circ}$ / -5 $^{\circ}$ of actual peak temperature	10 seconds					
Ramp-down rate	5°C / second max.					



#### **Standard Packaging**

Part Number	В	ulk	Reel/Tape		
Fait Number	Pcs/Box	P/N Code	Pcs/Reel	P/N Code	
0ZRR0005FF - 0ZRR0090FF	3000	1E	3000	2E	
0ZRR0110FF - 0ZRR0185FF	1000	1A	1500	2B	
0ZRR0250FF - 0ZRR0375FF	1000	1A	1000	2A	

## P/N Explanation and Ordering Information





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