



ATTILA

Instant-DevKit ARRIA 10 FPGA FMC IDK

The ARRIA 10 FPGA FMC **Instant-Development Kit** provides to developers the best Out-Of-The box experience combining the Best-In Class compact hardware platform and the most efficient intuitive software environment.

The target markets are as wide as High speed Data communication, High Performance Computing, Finance, Video capture and processing, Industrial, Military, Test & Measurement and Medical.

DevKitInvasion



ATTILA
ARRIA 10 FPGA FMC IDK

ALARIC
ARRIA 10 SoC 660 KLEs

ARMINIUS
ARRIA 10 FPGA FMC IDK + 4DSP FMC142



Contact us :

ReFLEX CES Europe
Phone : +33(0) 169 870 255

email : sales@reflexces.com
www.reflexces.com

ReFLEX CES North America / AsiaPac / Japan
Phone : +1 (408) 887 5981

© ReFLEX CES 2015 -All Rights Reserved- Rev2.0



Featured Device

ARRIA 10 GX 660 KLEs

- DevKit based on ALTERA FPGA 660 KLEs
F40 package is compatible for :
 - ARRIA 10 GX 570/660/900/1150 KLEs
 - ARRIA 10 GT
- PCIe edge connector for Gen3 x8 (64Gb/s)
- FMC High Pin Count fully compliant:
 - 168 LVCMOS (1.8V) or 84 LVDS (1.8V,2.5V)
 - 10 XCVR (up to 12.5Gb/s)*
- Extension connector SEARAY™ Series
 - 20 LVDS (1.8V) + 1 LVDS Clock
 - 22 XCVR (up to 17.4Gb/s per link)*
- QSFP+ optical cage (4 XCVR : 12.5 Gb/s per link)*
- One serial over USB High speed link (through USB Hub)
- RJ45 copper connector 10/100/1000Base-T Ethernet (Through RGMII PHY)
- DDR4 SODIMM interface (up to 16 GB, 3.2Gb/s support ECC and Non ECC)
- DisplayPort Rev1.2 (up to 5.4Gbit/s)
- MAX 10 Power supplies monitoring with its ADCs solution
- Innovative clock tree (Silicon Labs)
- +12V on a Jack/EdgePCle/PCle Aux connector
- Active Heat sink
- PCIe Bracket, LED

* = ES1 : up to 10.3125 Gbps per link

Board Programming

- JTAG : On-board USB-Blaster II MAX V (reachable by USB Hub)
- Fast passive parallel (FPP) configuration via MAX® 10 device and flash memory
- AS configuration supported with NOR FLASH EPCQL1024

Board Size

- Length : 240 mm (9.45 inches)
- Height : 111.15 mm (4.38 inches) / Standard Height PCle
- Thickness : 1.57 mm (0.062 inches) / Standard Thickness PCle

Clock Circuitry

- 3 SMA connectors (1 main input clock+1 output programmable clock)
- 7x FPGA XCVR programmable inputs Clocks (up to 800 MHz to reach different communication protocol)

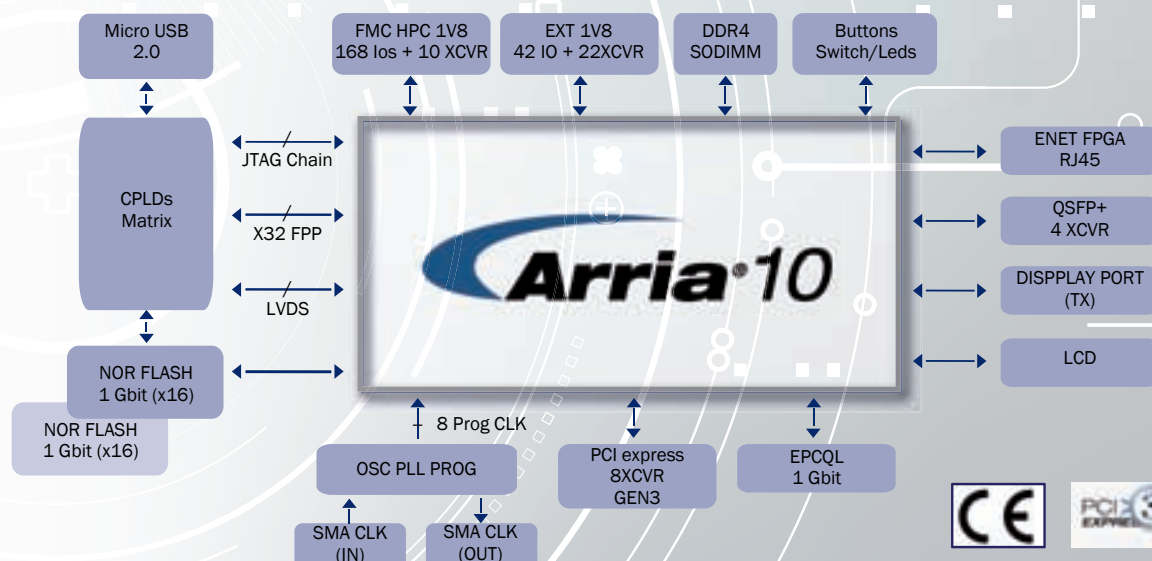
Kit Content

- Power adapter (US, UK, EU, JP) and cables (USB, Ethernet, DisplayPort, SMA)
- 4GB non-ECC DDR4 SODIMM module (Micron Technology)
- FMC, Extension connector and PCIe Loopback boards
- LCD (2 lines x 16 characters, I2C, 5V)
- Starter Guide
- ReFLEX CES innovative software interface (GUI) on USB Key/Windows64 with Test Designs

Kit Ordering information

- RXCA10X115AF40-IDK00A (Now, Engineering version 1- ask for erratas)
- RXCA10X115BF40-IDK00A (End Q2-2015, Engineering version 2- ask for erratas)
- RXCA10X066PF40-IDK00A (Q3-2015, Production version)

Partners



Contact us :



ReFLEX CES Europe
Phone : +33(0) 169 870 255

email : sales@reflexces.com
www.reflexces.com

ReFLEX CES North America / AsiaPac / Japan
Phone : +1 (408) 887 5981

© ReFLEX 2015 - All Rights Reserved - Rev2.0